

ET150396423VS

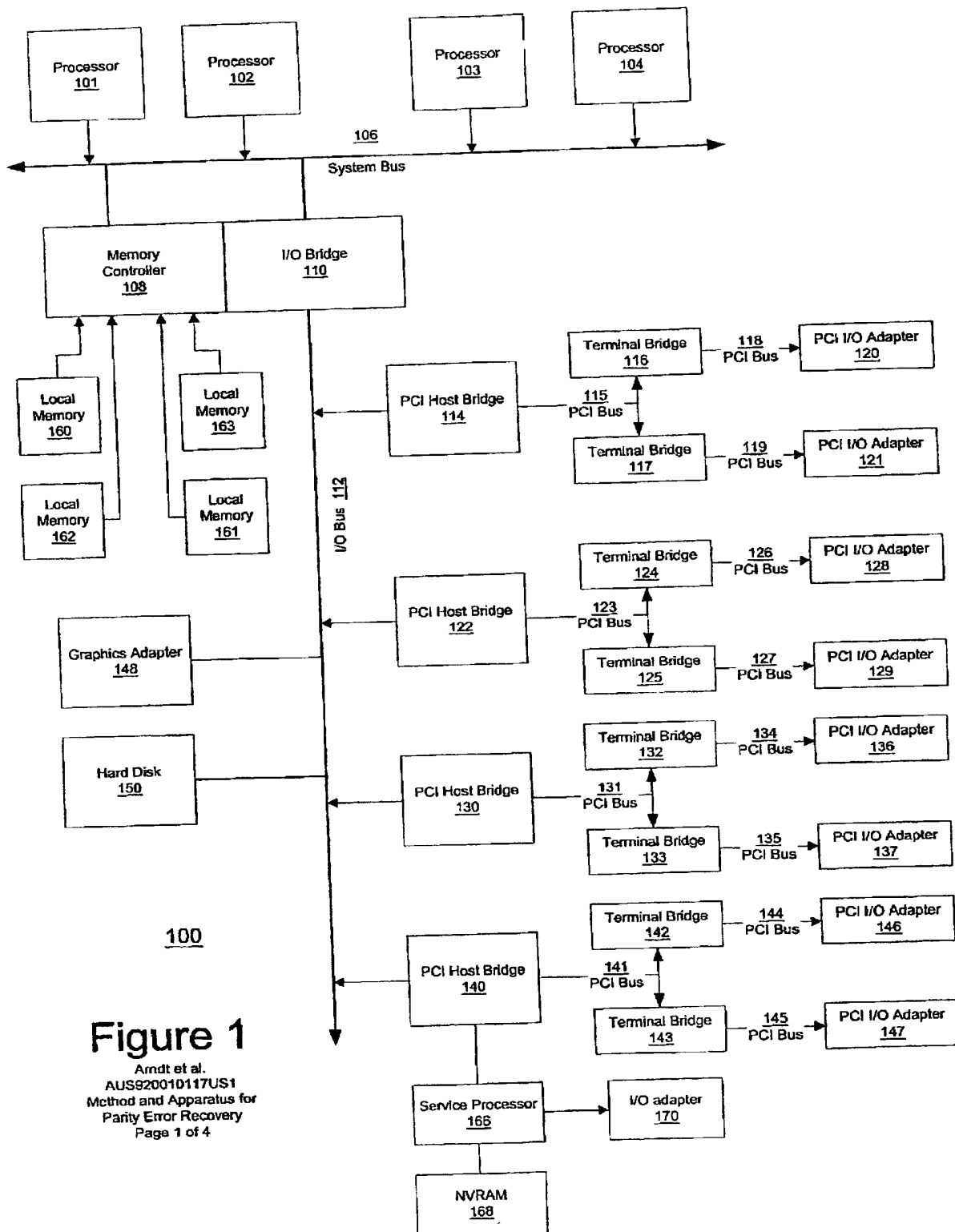


Figure 1
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FIG. 1

Figure 2

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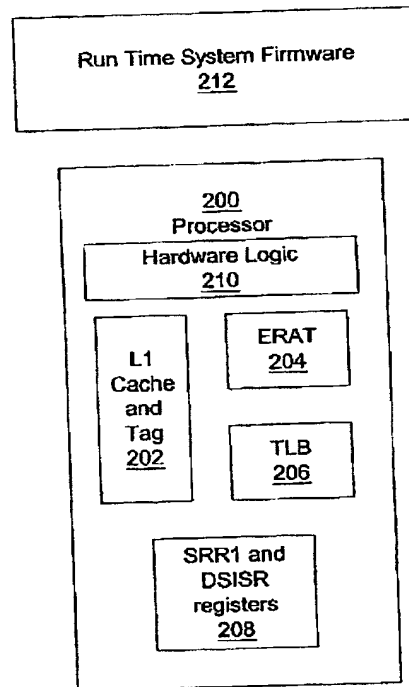


Figure 3

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	302	304	306	308	310
	Error Description	Hardware Indication	Firmware Recovery	Log Return Severity	Log Return RTAS Disposition
312	- Lost HW states	SRR1[R] = 0	None	Fatal (error not synchronous to current context)	Not Recovered
318	- D-cache parity - D-cache tag parity Under threshold	SRR1[R] = 1 & SRR1[43] = 1 & DSISR[18 / 19] = 1	Invalidate D-cache	Warning	Fully Recovered
320	- D-cache parity - D-cache tag parity Over threshold	same as above	- Invalidate D-cache - Disable failing portion of D-cache	Warning	Fully Recovered
322	- L/S TLB parity - L/S D-ERAT parity Under threshold	SRR1[R] = 1 & SRR1[43] = 1 & DSISR[20 / 21] = 1	Invalidate TLB all	Warning	Fully Recovered
314	- L/S TLB parity - L/S D-ERAT parity Over threshold	same as above	Invalidate TLB all, disable failing portion of ERAT or TLB	Warning	Fully Recovered
324	- Ifetch TLB parity Under threshold	SRR1[R] = 1 & SRR1[44:45] = 10	Invalidate TLB all	Warning	Fully Recovered
316	- Ifetch TLB parity Over threshold	same as above	Invalidate TLB all, disable failing portion of TLB	Warning	Fully Recovered

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Figure 4

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